

L Number	Hits	Search Text	DB	Time stamp
6	28	((("5812416") or ("5774380") or ("5666289") or ("5661662") or ("5644498") or ("5638380") or ("5617431") or ("5550839") or ("5526277") or ("5487018") or ("5452227") or ("5424589") or ("5384710") or ("5313615")).PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/25 08:09
7	2	"6425109"	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/25 08:11
10	30	((("5812416") or ("5774380") or ("5666289") or ("5661662") or ("5644498") or ("5638380") or ("5617431") or ("5550839") or ("5526277") or ("5487018") or ("5452227") or ("5424589") or ("5384710") or ("5313615")).PN.) or "6425109"	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/25 08:11
11	2	((binary adj1 decision adj1 diagram) or BDD or (binary adj2 diagram) or OBDD or Boolean) and (((("5812416") or ("5774380") or ("5666289") or ("5661662") or ("5644498") or ("5638380") or ("5617431") or ("5550839") or ("5526277") or ("5487018") or ("5452227") or ("5424589") or ("5384710") or ("5313615")).PN.) or "6425109")	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/25 10:35
19	199	(core same pin) and (classif\$7 or arrang\$5) and ((binary adj2 diagram) or boolean)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/25 09:06
20	22	(core same pin) and classif\$7 and ((binary adj2 diagram) or boolean)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/25 09:14
21	7	(core same classif\$7) and (pin same classif\$7) and ((binary adj2 diagram) or boolean)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/25 09:21
22	13	(core same classif\$7) and (node same classif\$7) and ((binary adj2 diagram) or boolean)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/25 09:30
23	13	(core same classif\$7) and (pin same classif\$7) and ((binary adj2 diagram) or boolean or BDD or OBDD or BED)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/25 09:33
24	6465	core and pin and ((binary adj2 diagram) or boolean or BDD or OBDD or BED)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/25 09:35
25	2206	(categoriz\$5 or classif\$7 or organiz\$3 or order\$3 or sort\$3 or grad\$3 or arrang\$3) same (core or pin) and (core and pin and ((binary adj2 diagram) or boolean or BDD or OBDD or BED))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/25 09:38
26	367	(categoriz\$5 or classif\$7 or organiz\$3 or order\$3 or sort\$3 or grad\$3 or arrang\$3) near2 (core or pin) and (core and pin and ((binary adj2 diagram) or boolean or BDD or OBDD or BED))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/25 10:20
27	55	match\$3 and (core same pin) and ((categoriz\$5 or classif\$7 or organiz\$3 or order\$3 or sort\$3 or grad\$3 or arrang\$3) near2 (core or pin) and (core and pin and ((binary adj2 diagram) or boolean or BDD or OBDD or BED)))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/25 10:03

28	122	match\$3 and ((core or block or cell or macrocell or cluster or unit or subcircuit or subset or sub-circuit or sub-set) same pin) and ((categoriz\$5 or classif\$7 or organiz\$3 or order\$3 or sort\$3 or grad\$3 or arrang\$3) near2 (core or pin) and (core and pin and ((binary adj2 diagram) or boolean or BDD or OBDD or BED)))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/25 10:07
29	6948	716/\$.cccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/25 10:08
30	8	(match\$3 and ((core or block or cell or macrocell or cluster or unit or subcircuit or subset or sub-circuit or sub-set) same pin) and ((categoriz\$5 or classif\$7 or organiz\$3 or order\$3 or sort\$3 or grad\$3 or arrang\$3) near2 (core or pin) and (core and pin and ((binary adj2 diagram) or boolean or BDD or OBDD or BED)))) and 716/\$.cccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/25 10:19
31	28	((("5870308") or ("5689432") or ("5541849") or ("5537328") or ("5502649") or ("5483461") or ("5418733") or ("5378904") or ("5349542") or ("5313079") or ("5047949") or ("4731643") or ("4638442") or ("4612618")).PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/25 10:20
32	6	"6083271"	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/25 10:20
33	34	((("5870308") or ("5689432") or ("5541849") or ("5537328") or ("5502649") or ("5483461") or ("5418733") or ("5378904") or ("5349542") or ("5313079") or ("5047949") or ("4731643") or ("4638442") or ("4612618")).PN.) or "6083271"	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/25 10:20
34	4	(categoriz\$5 or classif\$7 or organiz\$3 or order\$3 or sort\$3 or grad\$3 or arrang\$3) same (core or pin) and (((("5870308") or ("5689432") or ("5541849") or ("5537328") or ("5502649") or ("5483461") or ("5418733") or ("5378904") or ("5349542") or ("5313079") or ("5047949") or ("4731643") or ("4638442") or ("4612618")).PN.) or "6083271")	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/25 10:21
35	3	(BDD or (binary adj2 diagram) or OBDD or BED or Boolean) and ((categoriz\$5 or classif\$7 or organiz\$3 or order\$3 or sort\$3 or grad\$3 or arrang\$3) same (core or pin) and (((("5870308") or ("5689432") or ("5541849") or ("5537328") or ("5502649") or ("5483461") or ("5418733") or ("5378904") or ("5349542") or ("5313079") or ("5047949") or ("4731643") or ("4638442") or ("4612618")).PN.) or "6083271"))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/25 10:38
36	20	"4638442"	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/25 10:38

37	1	((BDD or (binary adj2 diagram) or OBDD or BED or Boolean) and ((categoriz\$5 or classif\$7 or organiz\$3 or order\$3 or sort\$3 or grad\$3 or arrang\$3) same (core or pin) and (((("5870308") or ("5689432") or ("5541849") or ("5537328") or ("5502649") or ("5483461") or ("5418733") or ("5378904") or ("5349542") or ("5313079") or ("5047949") or ("4731643") or ("4638442") or ("4612618")).PN.) or "6083271")) and "4638442"	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/25 10:47
38	8	variable and "4638442"	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/25 10:50
39	5	pin and (variable and "4638442")	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/25 10:51
-	2	("6477691").PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/23 14:25
-	2	("6324680").PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/23 14:29
-	2	("6163876").PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/23 14:31
-	2	("5748647").PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/23 14:33
-	4	((("6338158") or ("5870310")).PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/23 14:33
-	2	"6477691"	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/23 14:34
-	6	((("6338158") or ("5870310")).PN.) or "6477691"	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/23 14:35
-	2	((system adj1 on adj1 chip) or SOC) and pin and (((("6338158") or ("5870310")).PN.) or "6477691")	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/24 09:40
-	4	pin and (((("6338158") or ("5870310")).PN.) or "6477691")	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/23 14:42
-	419	((system adj1 on adj1 chip) or SOC) same pin	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/23 15:04
-	11129	((system adj1 on adj1 chip) or SOC) and pin	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/23 14:47
-	3347	((core or block or cell) same pin) and ((system adj1 on adj1 chip) or SOC) and pin)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/23 15:05

-	1	((assess\$3 or calculat\$3 or compute or computing or estimat\$3 or levy\$3 or reckon\$4 or apprais\$3 or judg\$3) with (compatib\$5 or consisten\$ or agreeing or conforming or accordan\$ or congruous or harmonious or cooperat\$4 or ageeable or consonan\$2 or corresponden\$2) with pin) and (((core or block or cell) same pin) and (((system adj1 on adj1 chip) or SOC) and pin))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/23 16:46
-	84	((compatib\$5 or consisten\$ or agreeing or conforming or accordan\$ or congruous or harmonious or cooperat\$4 or ageeable or consonan\$2 or corresponden\$2) with pin) and (((core or block or cell) same pin) and (((system adj1 on adj1 chip) or SOC) and pin))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/23 15:07
-	0	((binary adj1 decision adj1 diagram) or BDD) and (((compatib\$5 or consisten\$ or agreeing or conforming or accordan\$ or congruous or harmonious or cooperat\$4 or ageeable or consonan\$2 or corresponden\$2) with pin) and (((core or block or cell) same pin) and (((system adj1 on adj1 chip) or SOC) and pin)))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/24 08:22
-	0	((binary near3 diagram) or (decision near3 diagram) or BDD) and (((compatib\$5 or consisten\$ or agreeing or conforming or accordan\$ or congruous or harmonious or cooperat\$4 or ageeable or consonan\$2 or corresponden\$2) with pin) and (((core or block or cell) same pin) and (((system adj1 on adj1 chip) or SOC) and pin)))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/24 08:22
-	2423	((system adj1 on adj1 chip) or SOC) same (pin or output or input or I/O or input/output)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/23 16:34
-	1299	((core or block or cell) same (pin or output or input or I/O or input/output)) and (((system adj1 on adj1 chip) or SOC) same (pin or output or input or I/O or input/output))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/23 15:31
-	1	((assess\$3 or calculat\$3 or compute or computing or estimat\$3 or levy\$3 or reckon\$4 or apprais\$3 or judg\$3) with (compatib\$5 or consisten\$ or agreeing or conforming or accordan\$ or congruous or harmonious or cooperat\$4 or ageeable or consonan\$2 or corresponden\$2) with pin) and (((core or block or cell) same (pin or output or input or I/O or input/output)) and (((system adj1 on adj1 chip) or SOC) same (pin or output or input or I/O or input/output)))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/23 15:06
-	136	((compatib\$5 or consisten\$ or agreeing or conforming or accordan\$ or congruous or harmonious or cooperat\$4 or ageeable or consonan\$2 or corresponden\$2) with (pin or output or input or I/O or input/output)) and (((core or block or cell) same (pin or output or input or I/O or input/output)) and (((system adj1 on adj1 chip) or SOC) same (pin or output or input or I/O or input/output)))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/23 15:33

-	0	((binary adj1 decision adj1 diagram) or BDD) and (((compatib\$5 or consisten\$ or agreeing or conforming or accordan\$ or congruous or harmonious or cooperat\$4 or ageeable or consonan\$2 or corresponden\$2) with (pin or output or input or I/O or input/output)) and (((core or block or cell) same (pin or output or input or I/O or input/output)) and (((system adj1 on adj1 chip) or SOC) same (pin or output or input or I/O or input/output))))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/24 09:30
-	0	((binary near3 diagram) or (decision near3 diagram) or BDD) and (((compatib\$5 or consisten\$ or agreeing or conforming or accordan\$ or congruous or harmonious or cooperat\$4 or ageeable or consonan\$2 or corresponden\$2) with (pin or output or input or I/O or input/output)) and (((core or block or cell) same (pin or output or input or I/O or input/output)) and (((system adj1 on adj1 chip) or SOC) same (pin or output or input or I/O or input/output))))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/23 15:08
-	19	boolean and (((compatib\$5 or consisten\$ or agreeing or conforming or accordan\$ or congruous or harmonious or cooperat\$4 or ageeable or consonan\$2 or corresponden\$2) with (pin or output or input or I/O or input/output)) and (((core or block or cell) same (pin or output or input or I/O or input/output)) and (((system adj1 on adj1 chip) or SOC) same (pin or output or input or I/O or input/output))))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/23 15:09
-	69131	((system adj1 on adj1 chip) or SOC) and (pin or pad or terminal or output or input or I/O or input/output)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/23 15:30
-	74	((binary adj1 decision adj1 diagram) or BDD) and (((system adj1 on adj1 chip) or SOC) and (pin or pad or terminal or output or input or I/O or input/output))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/23 16:37
-	33	((core or block or cell or macrocell or cluster) same (pin or pad or terminal or output or input or I/O or input/output)) and (((binary adj1 decision adj1 diagram) or BDD) and (((system adj1 on adj1 chip) or SOC) and (pin or pad or terminal or output or input or I/O or input/output))))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/24 08:20
-	1	((compatib\$5 or consisten\$ or agreeing or conforming or accordan\$ or congruous or harmonious or cooperat\$4 or ageeable or consonan\$2 or corresponden\$2) with (pin or output or input or I/O or input/output)) and (((core or block or cell or macrocell or cluster) same (pin or pad or terminal or output or input or I/O or input/output)) and (((binary adj1 decision adj1 diagram) or BDD) and (((system adj1 on adj1 chip) or SOC) and (pin or pad or terminal or output or input or I/O or input/output))))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/23 16:39
-	50	((("5663888") or ("5629860") or ("5586046") or ("5555188") or ("5541849") or ("5530654") or ("5448497") or ("5436849") or ("5412591") or ("5349659") or ("5319570") or ("5297053") or ("5282140") or ("5220512") or ("5175693") or ("5119314") or ("5068823") or ("4868778") or ("4829446") or ("4827428") or ("4701860") or ("4652992") or ("4377849") or ("4093990") or ("3968478"))).PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/23 16:30

-	16	"5910898"	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/23 16:30
-	66	((("5663888") or ("5629860") or ("5586046") or ("5555188") or ("5541849") or ("5530654") or ("5448497") or ("5436849") or ("5412591") or ("5349659") or ("5319570") or ("5297053") or ("5282140") or ("5220512") or ("5175693") or ("5119314") or ("5068823") or ("4868778") or ("4829446") or ("4827428") or ("4701860") or ("4652992") or ("4377849") or ("4093990") or ("3968478")).PN.) or "5910898"	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/23 16:31
-	1	((system adj1 on adj1 chip) or SOC) and (((("5663888") or ("5629860") or ("5586046") or ("5555188") or ("5541849") or ("5530654") or ("5448497") or ("5436849") or ("5412591") or ("5349659") or ("5319570") or ("5297053") or ("5282140") or ("5220512") or ("5175693") or ("5119314") or ("5068823") or ("4868778") or ("4829446") or ("4827428") or ("4701860") or ("4652992") or ("4377849") or ("4093990") or ("3968478")).PN.) or "5910898")	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/23 16:31
-	72840	((system adj1 on adj1 chip) or SOC) and (pin or pad or terminal or node or port or output or input or I/O or input/output)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/23 16:37
-	76	((binary adj1 decision adj1 diagram) or BDD) and (((system adj1 on adj1 chip) or SOC) and (pin or pad or terminal or node or port or output or input or I/O or input/output))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/23 16:38
-	2	((compatib\$5 or consisten\$ or agreeing or conforming or accordan\$ or congruous or harmonious or cooperat\$4 or ageeable or consonan\$2 or corresponden\$2) with (pin or pad or terminal or node or port or output or input or I/O or input/output)) and (((binary adj1 decision adj1 diagram) or BDD) and (((system adj1 on adj1 chip) or SOC) and (pin or pad or terminal or node or port or output or input or I/O or input/output)))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/23 16:45
-	27251	((core or block or cell or macrocell or cluster) same (pin or pad or terminal or node or port or output or input or I/O or input/output)) and (((system adj1 on adj1 chip) or SOC) and (pin or pad or terminal or node or port or output or input or I/O or input/output))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/23 16:44
-	1561	((compatib\$5 or consisten\$ or agreeing or conforming or accordan\$ or congruous or harmonious or cooperat\$4 or ageeable or consonan\$2 or corresponden\$2) with (pin or pad or terminal or node or port or output or input or I/O or input/output)) and (((core or block or cell or macrocell or cluster) same (pin or pad or terminal or node or port or output or input or I/O or input/output)) and (((system adj1 on adj1 chip) or SOC) and (pin or pad or terminal or node or port or output or input or I/O or input/output)))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/23 16:45

-	1	((assess\$3 or calculat\$3 or compute or computing or estimat\$3 or levy\$3 or reckon\$4 or apprais\$3 or judg\$3) with (compatib\$5 or consisten\$ or agreeing or conforming or accordan\$ or congruous or harmonious or cooperat\$4 or ageeable or consonan\$2 or corresponden\$2) with (pin) and (((compatib\$5 or consisten\$ or agreeing or conforming or accordan\$ or congruous or harmonious or cooperat\$4 or ageeable or consonan\$2 or corresponden\$2) with (pin or pad or terminal or node or port or output or input or I/O or input/output)) and (((core or block or cell or macrocell or cluster) same (pin or pad or terminal or node or port or output or input or I/O or input/output)) and (((system adj1 on adj1 chip) or SOC) and (pin or pad or terminal or node or port or output or input or I/O or input/output))))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/23 16:47
-	16	((assess\$3 or calculat\$3 or compute or computing or estimat\$3 or levy\$3 or reckon\$4 or apprais\$3 or judg\$3) with (compatib\$5 or consisten\$ or agreeing or conforming or accordan\$ or congruous or harmonious or cooperat\$4 or ageeable or consonan\$2 or corresponden\$2) with (pin or pad or terminal or node)) and (((compatib\$5 or consisten\$ or agreeing or conforming or accordan\$ or congruous or harmonious or cooperat\$4 or ageeable or consonan\$2 or corresponden\$2) with (pin or pad or terminal or node or port or output or input or I/O or input/output)) and (((core or block or cell or macrocell or cluster) same (pin or pad or terminal or node or port or output or input or I/O or input/output)) and (((system adj1 on adj1 chip) or SOC) and (pin or pad or terminal or node or port or output or input or I/O or input/output))))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/23 16:56
-	1479	((core or block or cell or macrocell or cluster) same (pin or pad or terminal or output or input or I/O or input/output)) and (((compatib\$5 or consisten\$ or agreeing or conforming or accordan\$ or congruous or harmonious or cooperat\$4 or ageeable or consonan\$2 or corresponden\$2) with (pin or pad or terminal or node or port or output or input or I/O or input/output)) and (((core or block or cell or macrocell or cluster) same (pin or pad or terminal or node or port or output or input or I/O or input/output)) and (((system adj1 on adj1 chip) or SOC) and (pin or pad or terminal or node or port or output or input or I/O or input/output))))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/23 16:55

-	1330	(assess\$3 or calculat\$3 or compute or computing or estimat\$3 or levy\$3 or reckon\$4 or apprais\$3 or judg\$3) and (((compatib\$5 or consisten\$ or agreeing or conforming or accordan\$ or congruous or harmonious or cooperat\$4 or ageeable or consonan\$2 or corresponden\$2) with (pin or pad or terminal or node or port or output or input or I/O or input/output)) and (((core or block or cell or macrocell or cluster) same (pin or pad or terminal or node or port or output or input or I/O or input/output)) and ((system adj1 on adj1 chip) or SOC) and (pin or pad or terminal or node or port or output or input or I/O or input/output))))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/23 16:57
-	6925	716/\$.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/23 16:57
-	40	((assess\$3 or calculat\$3 or compute or computing or estimat\$3 or levy\$3 or reckon\$4 or apprais\$3 or judg\$3) and (((compatib\$5 or consisten\$ or agreeing or conforming or accordan\$ or congruous or harmonious or cooperat\$4 or ageeable or consonan\$2 or corresponden\$2) with (pin or pad or terminal or node or port or output or input or I/O or input/output)) and (((core or block or cell or macrocell or cluster) same (pin or pad or terminal or node or port or output or input or I/O or input/output)) and ((system adj1 on adj1 chip) or SOC) and (pin or pad or terminal or node or port or output or input or I/O or input/output)))) and 716/\$.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/23 16:58
-	59	(compatib\$5 or compliance or consisten\$ or agreeing or conforming or accordan\$ or congruous or harmonious or cooperat\$4 or ageeable or consonan\$2 or corresponden\$2) with pin with (assess\$3 or calculat\$3 or compute or computing or estimat\$3 or levy\$3 or reckon\$4 or apprais\$3 or judg\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/24 12:36
-	0	((binary adj1 decision adj1 diagram) or BDD) and (((core or block or cell or macrocell or cluster) same (pin or node or port or pad or terminal or output or input or I/O or input/output)) and ((compatib\$5 or compliance or consisten\$ or agreeing or conforming or accordan\$ or congruous or harmonious or cooperat\$4 or ageeable or consonan\$2 or corresponden\$2) with pin with (assess\$3 or calculat\$3 or compute or computing or estimat\$3 or levy\$3 or reckon\$4 or apprais\$3 or judg\$3)))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/24 08:25
-	0	((binary near3 diagram) or (decision near3 diagram) or BDD) and (((core or block or cell or macrocell or cluster) same (pin or node or port or pad or terminal or output or input or I/O or input/output)) and ((compatib\$5 or compliance or consisten\$ or agreeing or conforming or accordan\$ or congruous or harmonious or cooperat\$4 or ageeable or consonan\$2 or corresponden\$2) with pin with (assess\$3 or calculat\$3 or compute or computing or estimat\$3 or levy\$3 or reckon\$4 or apprais\$3 or judg\$3)))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/24 08:22

-	0	((binary near3 diagram) or (decision near3 diagram) or BDD) and ((compatib\$5 or compliance or consisten\$ or agreeing or conforming or accordan\$ or congruous or harmonious or cooperat\$4 or ageeable or consonan\$2 or corresponden\$2) with pin with (assess\$3 or calculat\$3 or compute or computing or estimat\$3 or levy\$3 or reckon\$4 or apprais\$3 or judg\$3))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/24 08:25
-	165	((compatib\$5 or compliance or consisten\$ or agreeing or conforming or accordan\$ or congruous or harmonious or cooperat\$4 or ageeable or consonan\$2 or corresponden\$2) with pin) same (assess\$3 or calculat\$3 or compute or computing or estimat\$3 or levy\$3 or reckon\$4 or apprais\$3 or judg\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/24 09:27
-	70	((core or block or cell or macrocell or cluster) same (pin or node or port or pad or terminal or output or input or I/O or input/output)) and (((compatib\$5 or compliance or consisten\$ or agreeing or conforming or accordan\$ or congruous or harmonious or cooperat\$4 or ageeable or consonan\$2 or corresponden\$2) with pin) same (assess\$3 or calculat\$3 or compute or computing or estimat\$3 or levy\$3 or reckon\$4 or apprais\$3 or judg\$3))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/24 12:38
-	0	((binary adj1 decision adj1 diagram) or BDD) and (((core or block or cell or macrocell or cluster) same (pin or node or port or pad or terminal or output or input or I/O or input/output)) and (((compatib\$5 or compliance or consisten\$ or agreeing or conforming or accordan\$ or congruous or harmonious or cooperat\$4 or ageeable or consonan\$2 or corresponden\$2) with pin) same (assess\$3 or calculat\$3 or compute or computing or estimat\$3 or levy\$3 or reckon\$4 or apprais\$3 or judg\$3)))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/24 08:25
-	2	((binary near3 diagram) or (decision near3 diagram) or BDD) and ((core or block or cell or macrocell or cluster) same (pin or node or port or pad or terminal or output or input or I/O or input/output)) and (((compatib\$5 or compliance or consisten\$ or agreeing or conforming or accordan\$ or congruous or harmonious or cooperat\$4 or ageeable or consonan\$2 or corresponden\$2) with pin) same (assess\$3 or calculat\$3 or compute or computing or estimat\$3 or levy\$3 or reckon\$4 or apprais\$3 or judg\$3)))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/24 08:25
-	26	((core or block or cell or macrocell or cluster) same (pin or node or port or pad or terminal or output or input or I/O or input/output)) and ((compatib\$5 or compliance or consisten\$ or agreeing or conforming or accordan\$ or congruous or harmonious or cooperat\$4 or ageeable or consonan\$2 or corresponden\$2) with pin with (assess\$3 or calculat\$3 or compute or computing or estimat\$3 or levy\$3 or reckon\$4 or apprais\$3 or judg\$3))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/24 09:26
-	55	"4928278"	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/24 09:26

-	1	((compatib\$5 or compliance or consisten\$ or agreeing or conforming or accordan\$ or congruous or harmonious or cooperat\$4 or ageeable or consonan\$2 or corresponden\$2) with pin) same (assess\$3 or calculat\$3 or compute or computing or estimat\$3 or levy\$3 or reckon\$4 or apprais\$3 or judg\$3)) and "4928278"	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/24 09:27
-	216	((binary adj1 decision adj1 diagram) or BDD) and ((compatib\$5 or relat\$ or compliance or consisten\$ or agreeing or conforming or accordan\$ or congruous or harmonious or cooperat\$4 or ageeable or consonan\$2 or corresponden\$2) with (pin or pad or terminal or node or port or output or input or I/O or input/output)) and ((core or block or cell or macrocell or unit or cluster) same (pin or pad or terminal or node or port or output or input or I/O or input/output))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/24 11:00
-	108	((pin or pad or terminal or node or port or output or input or I/O or input/output) with (assess\$3 or calculat\$3 or compute or computing or estimat\$3 or levy\$3 or reckon\$4 or apprais\$3 or judg\$3)) and (((binary adj1 decision adj1 diagram) or BDD) and ((compatib\$5 or relat\$ or compliance or consisten\$ or agreeing or conforming or accordan\$ or congruous or harmonious or cooperat\$4 or ageeable or consonan\$2 or corresponden\$2) with (pin or pad or terminal or node or port or output or input or I/O or input/output)) and ((core or block or cell or macrocell or unit or cluster) same (pin or pad or terminal or node or port or output or input or I/O or input/output)))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/24 10:59
-	5	((system adj1 on adj chip) or SOC) and (((pin or pad or terminal or node or port or output or input or I/O or input/output) with (assess\$3 or calculat\$3 or compute or computing or estimat\$3 or levy\$3 or reckon\$4 or apprais\$3 or judg\$3)) and (((binary adj1 decision adj1 diagram) or BDD) and ((compatib\$5 or relat\$ or compliance or consisten\$ or agreeing or conforming or accordan\$ or congruous or harmonious or cooperat\$4 or ageeable or consonan\$2 or corresponden\$2) with (pin or pad or terminal or node or port or output or input or I/O or input/output)) and ((core or block or cell or macrocell or unit or cluster) same (pin or pad or terminal or node or port or output or input or I/O or input/output)))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/24 10:57

-	5	((on adj2 chip) or SOC) and (((pin or pad or terminal or node or port or output or input or I/O or input/output) with (assess\$3 or calculat\$3 or compute or computing or estimat\$3 or levy\$3 or reckon\$4 or apprais\$3 or judg\$3)) and (((binary adj1 decision adj1 diagram) or BDD) and ((compatib\$5 or relat\$ or compliance or consisten\$ or agreeing or conforming or accordan\$ or congruous or harmonious or cooperat\$4 or ageeable or consonan\$2 or corresponden\$2) with (pin or pad or terminal or node or port or output or input or I/O or input/output)) and ((core or block or cell or macrocell or unit or cluster) same (pin or pad or terminal or node or port or output or input or I/O or input/output))))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/24 10:53
-	50	((("5663888") or ("5629860") or ("5586046") or ("5555188") or ("5541849") or ("5530654") or ("5448497") or ("5436849") or ("5412591") or ("5349659") or ("5319570") or ("5297053") or ("5282140") or ("5220512") or ("5175693") or ("5119314") or ("5068823") or ("4868778") or ("4829446") or ("4827428") or ("4701860") or ("4652992") or ("4377849") or ("4093990") or ("3968478"))).PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/24 10:52
-	16	"5910898"	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/24 10:52
-	66	((("5663888") or ("5629860") or ("5586046") or ("5555188") or ("5541849") or ("5530654") or ("5448497") or ("5436849") or ("5412591") or ("5349659") or ("5319570") or ("5297053") or ("5282140") or ("5220512") or ("5175693") or ("5119314") or ("5068823") or ("4868778") or ("4829446") or ("4827428") or ("4701860") or ("4652992") or ("4377849") or ("4093990") or ("3968478"))).PN.) or "5910898"	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/24 10:53
-	1	((on adj2 chip) or SOC) and (((("5663888") or ("5629860") or ("5586046") or ("5555188") or ("5541849") or ("5530654") or ("5448497") or ("5436849") or ("5412591") or ("5349659") or ("5319570") or ("5297053") or ("5282140") or ("5220512") or ("5175693") or ("5119314") or ("5068823") or ("4868778") or ("4829446") or ("4827428") or ("4701860") or ("4652992") or ("4377849") or ("4093990") or ("3968478"))).PN.) or "5910898")	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/24 10:53
-	385977	((compatib\$5 or relat\$ or compliance or consisten\$ or agreeing or conforming or accordan\$ or congruous or harmonious or cooperat\$4 or ageeable or consonan\$2 or corresponden\$2) with (pin or pad or terminal or node or port or output or input or I/O or input/output)) and ((core or block or cell or macrocell or unit or cluster) same (pin or pad or terminal or node or port or output or input or I/O or input/output))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/24 13:17

-	6952	((system adj2 on adj2 chip) or (on adj2 chip) or SOC) and (((compatib\$5 or relat\$ or compliance or consisten\$ or agreeing or conforming or accordan\$ or congruous or harmonious or cooperat\$4 or ageeable or consonan\$2 or corresponden\$2) with (pin or pad or terminal or node or port or output or input or I/O or input/output)) and ((core or block or cell or macrocell or unit or cluster) same (pin or pad or terminal or node or port or output or input or I/O or input/output)))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/24 11:14
-	2242	((pin or pad or terminal or node or port or output or input or I/O or input/output) with (assess\$3 or calculat\$3 or compute or computing or estimat\$3 or levy\$3 or reckon\$4 or apprais\$3 or judg\$3)) and (((system adj2 on adj2 chip) or (on adj2 chip) or SOC) and (((compatib\$5 or relat\$ or compliance or consisten\$ or agreeing or conforming or accordan\$ or congruous or harmonious or cooperat\$4 or ageeable or consonan\$2 or corresponden\$2) with (pin or pad or terminal or node or port or output or input or I/O or input/output)) and ((core or block or cell or macrocell or unit or cluster) same (pin or pad or terminal or node or port or output or input or I/O or input/output)))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/24 11:18
-	5	((binary adj1 decision adj1 diagram) or BDD) and (((pin or pad or terminal or node or port or output or input or I/O or input/output) with (assess\$3 or calculat\$3 or compute or computing or estimat\$3 or levy\$3 or reckon\$4 or apprais\$3 or judg\$3)) and (((system adj2 on adj2 chip) or (on adj2 chip) or SOC) and (((compatib\$5 or relat\$ or compliance or consisten\$ or agreeing or conforming or accordan\$ or congruous or harmonious or cooperat\$4 or ageeable or consonan\$2 or corresponden\$2) with (pin or pad or terminal or node or port or output or input or I/O or input/output)) and ((core or block or cell or macrocell or unit or cluster) same (pin or pad or terminal or node or port or output or input or I/O or input/output)))))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/24 12:42
-	6925	716/\$.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/24 11:01
-	58	((pin or pad or terminal or node or port or output or input or I/O or input/output) with (assess\$3 or calculat\$3 or compute or computing or estimat\$3 or levy\$3 or reckon\$4 or apprais\$3 or judg\$3)) and (((system adj2 on adj2 chip) or (on adj2 chip) or SOC) and (((compatib\$5 or relat\$ or compliance or consisten\$ or agreeing or conforming or accordan\$ or congruous or harmonious or cooperat\$4 or ageeable or consonan\$2 or corresponden\$2) with (pin or pad or terminal or node or port or output or input or I/O or input/output)) and ((core or block or cell or macrocell or unit or cluster) same (pin or pad or terminal or node or port or output or input or I/O or input/output))))) and 716/\$.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/24 11:03

-	8	boolean and (((pin or pad or terminal or node or port or output or input or I/O or input/output) with (assess\$3 or calculat\$3 or compute or computing or estimat\$3 or levy\$3 or reckon\$4 or apprais\$3 or judg\$3)) and (((system adj2 on adj2 chip) or (on adj2 chip) or SOC) and (((compatib\$5 or relat\$ or compliance or consisten\$ or agreeing or conforming or accordan\$ or congruous or harmonious or cooperat\$4 or ageeable or consonan\$2 or corresponden\$2) with (pin or pad or terminal or node or port or output or input or I/O or input/output)) and ((core or block or cell or macrocell or unit or cluster) same (pin or pad or terminal or node or port or output or input or I/O or input/output))))) and 716/\$.ccls.)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/24 12:42
-	202523	((system adj1 on adj1 chip) or (on adj1 chip) or on-chip or SOC)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/24 16:03
-	13152	((compatib\$5 or relat\$ or compliance or consisten\$ or agreeing or conforming or accordan\$ or congruous or harmonious or cooperat\$4 or ageeable or consonan\$2 or corresponden\$2) with (pin or pad or terminal or node or port or output or input or I/O or input/output)) and ((core or block or cell or macrocell or unit or cluster) same (pin or pad or terminal or node or port or output or input or I/O or input/output))) and (((system adj1 on adj1 chip) or (on adj1 chip) or on-chip or SOC))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/24 11:17
-	3880	((pin or pad or terminal or node or port or output or input or I/O or input/output) with (assess\$3 or calculat\$3 or compute or computing or estimat\$3 or levy\$3 or reckon\$4 or apprais\$3 or judg\$3)) and (((compatib\$5 or relat\$ or compliance or consisten\$ or agreeing or conforming or accordan\$ or congruous or harmonious or cooperat\$4 or ageeable or consonan\$2 or corresponden\$2) with (pin or pad or terminal or node or port or output or input or I/O or input/output)) and ((core or block or cell or macrocell or unit or cluster) same (pin or pad or terminal or node or port or output or input or I/O or input/output))) and (((system adj1 on adj1 chip) or (on adj1 chip) or on-chip or SOC)))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/24 11:18

-	7	((binary adj1 decision adj1 diagram) or BDD) and (((pin or pad or terminal or node or port or output or input or I/O or input/output) with (assess\$3 or calculat\$3 or compute or computing or estimat\$3 or levy\$3 or reckon\$4 or apprais\$3 or judg\$3)) and (((compatib\$5 or relat\$ or compliance or consisten\$ or agreeing or conforming or accordan\$ or congruous or harmonious or cooperat\$4 or ageeable or consonan\$2 or corresponden\$2) with (pin or pad or terminal or node or port or output or input or I/O or input/output)) and ((core or block or cell or macrocell or unit or cluster) same (pin or pad or terminal or node or port or output or input or I/O or input/output))) and (((system adj1 on adj1 chip) or (on adj1 chip) or on-chip or SOC))))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/24 15:11
-	805	(compatib\$5 or compliance or consisten\$ or agreeing or conforming or accordan\$ or congruous or harmonious or cooperat\$4 or ageeable or consonan\$2 or corresponden\$2) with (pin or pad or port or terminal) with (assess\$3 or calculat\$3 or compute or computing or estimat\$3 or levy\$3 or reckon\$4 or apprais\$3 or judg\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/24 13:24
-	435	((core or block or cell or macrocell or cluster or unit or cluster or subcircuit or subset or sub-circuit or sub-set) same (pin or node or port or pad or terminal)) and ((compatib\$5 or compliance or consisten\$ or agreeing or conforming or accordan\$ or congruous or harmonious or cooperat\$4 or ageeable or consonan\$2 or corresponden\$2) with (pin or pad or port or terminal) with (assess\$3 or calculat\$3 or compute or computing or estimat\$3 or levy\$3 or reckon\$4 or apprais\$3 or judg\$3))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/24 13:13
-	28	((system adj1 on adj1 chip) or (on adj1 chip) or on-chip or SOC)) and (((core or block or cell or macrocell or cluster or unit or cluster or subcircuit or subset or sub-circuit or sub-set) same (pin or node or port or pad or terminal)) and ((compatib\$5 or compliance or consisten\$ or agreeing or conforming or accordan\$ or congruous or harmonious or cooperat\$4 or ageeable or consonan\$2 or corresponden\$2) with (pin or pad or port or terminal) with (assess\$3 or calculat\$3 or compute or computing or estimat\$3 or levy\$3 or reckon\$4 or apprais\$3 or judg\$3)))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/24 12:42
-	0	((binary adj1 decision adj1 diagram) or BDD) and (((system adj1 on adj1 chip) or (on adj1 chip) or on-chip or SOC)) and (((core or block or cell or macrocell or cluster or unit or cluster or subcircuit or subset or sub-circuit or sub-set) same (pin or node or port or pad or terminal)) and ((compatib\$5 or compliance or consisten\$ or agreeing or conforming or accordan\$ or congruous or harmonious or cooperat\$4 or ageeable or consonan\$2 or corresponden\$2) with (pin or pad or port or terminal) with (assess\$3 or calculat\$3 or compute or computing or estimat\$3 or levy\$3 or reckon\$4 or apprais\$3 or judg\$3))))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/24 12:42

-	13	boolean and (((system adj1 on adj1 chip) or (on adj1 chip) or on-chip or SOC)) and (((core or block or cell or macrocell or cluster or unit or cluster or subcircuit or subset or sub-circuit or sub-set) same (pin or node or port or pad or terminal)) and ((compatib\$5 or compliance or consisten\$ or agreeing or conforming or accordan\$ or congruous or harmonious or cooperat\$4 or ageeable or consonan\$2 or corresponden\$2) with (pin or pad or port or terminal) with (assess\$3 or calculat\$3 or compute or computing or estimat\$3 or levy\$3 or reckon\$4 or apprais\$3 or judg\$3))))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/24 12:42
-	1434579	((core or block or cell or macrocell or cluster or unit or subcircuit or subset or sub-circuit or sub-set) with (interfac\$3 or connect\$3 or interconnect\$3 or (output and input) or I/O or input/orput))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/25 10:05
-	364487	((compatib\$5 or relat\$ or compliance or consisten\$ or agreeing or agreeable or "same" or similar) with (pin or pad or terminal or node or port or output or input or I/O or input/output)) and (((core or block or cell or macrocell or cluster or unit or subcircuit or subset or sub-circuit or sub-set) with (interfac\$3 or connect\$3 or interconnect\$3 or (output and input) or I/O or input/orput)))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/24 13:22
-	16734	(pin or pad or port or terminal) with (assess\$3 or calculat\$3 or compute or computing or estimat\$3 or levy\$3 or reckon\$4 or apprais\$3 or judg\$3) and (((compatib\$5 or relat\$ or compliance or consisten\$ or agreeing or agreeable or "same" or similar) with (pin or pad or terminal or node or port or output or input or I/O or input/output)) and (((core or block or cell or macrocell or cluster or unit or subcircuit or subset or sub-circuit or sub-set) with (interfac\$3 or connect\$3 or interconnect\$3 or (output and input) or I/O or input/orput))))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/24 15:10
-	1428	((pin or pad or port or terminal) with (assess\$3 or calculat\$3 or compute or computing or estimat\$3 or levy\$3 or reckon\$4 or apprais\$3 or judg\$3) and (((compatib\$5 or relat\$ or compliance or consisten\$ or agreeing or agreeable or "same" or similar) with (pin or pad or terminal or node or port or output or input or I/O or input/output)) and (((core or block or cell or macrocell or cluster or unit or subcircuit or subset or sub-circuit or sub-set) with (interfac\$3 or connect\$3 or interconnect\$3 or (output and input) or I/O or input/orput)))) and (((system adj1 on adj1 chip) or (on adj1 chip) or on-chip or SOC))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/24 15:10

-	0	((binary adj1 decision adj1 diagram) or BDD) and (((pin or pad or port or terminal) with (assess\$3 or calculat\$3 or compute or computing or estimat\$3 or levy\$3 or reckon\$4 or apprais\$3 or judg\$3) and (((compatib\$5 or relat\$ or compliance or consisten\$ or agreeing or agreeable or "same" or similar) with (pin or pad or terminal or node or port or output or input or I/O or input/output)) and (((core or block or cell or macrocell or cluster or unit or subcircuit or subset or sub-circuit or sub-set) with (interfac\$3 or connect\$3 or interconnect\$3 or (output and input) or I/O or input/orput)))) and (((system adj1 on adj1 chip) or (on adj1 chip) or on-chip or SOC)))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/24 15:11
-	125	boolean and (((pin or pad or port or terminal) with (assess\$3 or calculat\$3 or compute or computing or estimat\$3 or levy\$3 or reckon\$4 or apprais\$3 or judg\$3) and (((compatib\$5 or relat\$ or compliance or consisten\$ or agreeing or agreeable or "same" or similar) with (pin or pad or terminal or node or port or output or input or I/O or input/output)) and (((core or block or cell or macrocell or cluster or unit or subcircuit or subset or sub-circuit or sub-set) with (interfac\$3 or connect\$3 or interconnect\$3 or (output and input) or I/O or input/orput)))) and (((system adj1 on adj1 chip) or (on adj1 chip) or on-chip or SOC)))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/24 15:17
-	32	716/\$.cccls. and (boolean and (((pin or pad or port or terminal) with (assess\$3 or calculat\$3 or compute or computing or estimat\$3 or levy\$3 or reckon\$4 or apprais\$3 or judg\$3) and (((compatib\$5 or relat\$ or compliance or consisten\$ or agreeing or agreeable or "same" or similar) with (pin or pad or terminal or node or port or output or input or I/O or input/output)) and (((core or block or cell or macrocell or cluster or unit or subcircuit or subset or sub-circuit or sub-set) with (interfac\$3 or connect\$3 or interconnect\$3 or (output and input) or I/O or input/orput)))) and (((system adj1 on adj1 chip) or (on adj1 chip) or on-chip or SOC))))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/24 15:58
-	18	match\$3 and ((interconnect\$3 adj4 core) same pin)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/24 15:58
-	96	(interconnect\$3 adj4 core) same pin	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/25 09:01
-	2	compliance and ((interconnect\$3 adj4 core) same pin)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/24 16:00
-	25	consist\$4 and ((interconnect\$3 adj4 core) same pin)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/24 16:05
-	10	((system adj1 on adj1 chip) or (on adj1 chip) or on-chip or SOC) and ((interconnect\$3 adj4 core) same pin)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/24 16:04

-	3	(consist\$4 or compliance) and (((system adj1 on adj1 chip) or (on adj1 chip) or on-chip or SOC) and ((interconnect\$3 adj4 core) same pin))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/24 16:09
-	3	(match\$3 and ((interconnect\$3 adj4 core) same pin)) and ((consist\$4 or compliance) and (((system adj1 on adj1 chip) or (on adj1 chip) or on-chip or SOC) and ((interconnect\$3 adj4 core) same pin)))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/03/24 16:10